

Coordinator:

Dr. R.K.Sharma
A.P, ECE Department

Faculty For The Course

- Dr. A.K.Gupta (Prof. ECE)
- Dr. R.K.Sharma (A.P. ECE)
- Faculty and staff of SMDP-II
- Guest Faculty from TEQIP and other institutions.

Note:

- Brochure and application form can also be downloaded from our website:
<http://www.nitkr.ac.in>
- No. of seats available are 20
- List of selected participants will be displayed on the institute website mentioned above on 20.12.2007

Address for Correspondence:

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About NIT Kurukshetra

National Institute of Technology Kurukshetra, Haryana(founded a Regional Engineering College, Kurukshetra) is a premier Technical Institute of the region. Since 1963, like other Regional Engineering Colleges of India this institution too, had been a joint enterprise of the State and Central Governments. This Institute was conferred upon status of Deemed University on June 26, 2002. Since then it has been renamed as National Institute of Technology, Kurukshetra. (Deemed University). Presently the Institute offers bachelor degree courses in electrical, mechanical, civil, electronics and communication, computers, IT and industrial engineering & management. The institute also offers PG courses in engineering in fifteen different specializations. The Institute has good research ambient supporting a large number of research scholars pursuing their Ph.D. degrees.

Kurukshetra is a railway junction on the Delhi-Karnal-Ambala section of the Northern Railway. It is about 160 kms. from Delhi. The Institute campus is about 10 kms. from Pipli, a well known road junction on the Sher Shah Suri Marg and about 5km from Kurukshetra Railway station.



Short Term Training Programme (Under TEQIP)

On

**Digital SIC Design Using Verilog
(Dec 31,2007 –Jan 04, 2008)**



Organized by

**Department of
Electronics and Commn. Engg.
(Project SMDP-II)**

**National Institute of Technology
Kurukshetra**

**Coordinator
Dr. R.K.Sharma**

INTRODUCTION:

The developments in electronics and communication engineering in the last few decades have been almost totally in the area of Microelectronics. The developments in Microelectronics (processing technology and IC design) have resulted in unprecedented pace of growth in other areas of electronics and communication engineering.

The department of electronics and communication engineering is presently participating in project SMDP-II (Special Manpower Development Programme in VLSI design and related software), funded by ministry of IT government of India. Under the project the department has undertaken to generate manpower in VLSI design at different levels (UG, PG and Ph.D)

OBJECTIVE OF THE COURSE:

The objective of the course is to familiarize with the Digital IC design methodologies. The training programme aims at providing a clear overview of IC design process in general. The course emphasizes on the digital IC design using programmable devices like FPGAs.

COURSE CONTENT:

The main topics proposed to be covered in the programme are as given below:

- VLSI Design methodologies.
- Essentials of Verilog
- Digital Design using Verilog
- Implementation of digital ICs on FPGAs—A design project

ELIGIBILITY:

The course is open for the faculty members of engineering colleges and also polytechnics (participating in TEQIP programme), interested in the area.

REGISTRATION FEE:

- Per participant registration fee is Rs. 500/-.
- Lodging and boarding to be provided by the institute free of cost
- Registration form in the prescribed format along with the registration fee in the form of DD in favor of Director, NIT Kurukshetra, payable at SBI, NIT kurukshetra should reach the coordinator on or before the last date.

IMPORTANT DATES:

- Last date for Submission of registration form : 17th Dec.2007.
- Intimation of acceptance: on or before 20th Dec.,2007.

REGISTRATION FORM Short Term Training Programme (Under TEQIP)

On
Digital SIC Design
(Dec 31,2007 –Jan 04, 2008)
NIT KURUKSHETRA-136 119

1. Name Mr./Ms/ Mrs -----
2. Designation and official address

3. Email:-----
4. Phone----- (O)----- (R)
Mob. -----.
5. Educational qualification & specialization-

- 6 Professional experience-----
7. Name of the institution-----

8. Details of registration fee:
Amount (Rs):-----
DD No.: -----
Date: -----
Bank: -----
- 9 Lodging & boarding required:
Yes/ No

Signature of the applicant (with date)

Signature of Head of Dept./ instt. (with seal)